

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Gilbert Wolrich et al. Art Unit : 2154
Serial No. : 10/069,229 Examiner : Joshua Joo
Filed : December 11, 2002 Conf. No. : 1429
Title : BRANCH INSTRUCTION FOR PROCESSOR ARCHITECTURE

MAIL STOP APPEAL BRIEF – PATENTS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

STATEMENT OF SUBSTANCE OF INTERVIEW

In response to Appellant's Appeal Brief, Appellant received from the Examiner the "Examiner's Answer," dated December 13, 2006. The December 13, 2006, Examiner's Answer, Appellant prepared and filed on or about January 29, 2007, the Appellant's Reply Brief.

Appellant subsequently received from the Examiner another communication entitled "Examiner's Answer," dated February 26, 2007. Appellant's undersigned attorney contacted the Examiner on or about the middle of March, 2007, to determine the reasons that a second Examiner's Answer was mailed to Appellant. The Examiner advised Appellant's undersigned attorney that the first Examiner's Answer was missing the section "Related Proceeding(s) and Appendix" and therefore a supplemental Examiner's Answer, which included that section, was prepared and mailed to Appellant. The Examiner further advised that a supplemental Reply Brief, responding to the Supplemental Examiner's Answer, was not required.

Appellant nevertheless would like to take this opportunity to reiterate that the final rejections of the pending claims for the above-identified application should be reversed for the reasons stated in Appellant's September 14, 2006, Appeal Brief, and in the January 29, 2007, Reply Brief.

No fee is believed due. Please apply any required fees to deposit account 06-1050, referencing the attorney docket number shown above.

Respectfully submitted,

Date:

April 26, 2007

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